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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD AND STRUCTURE FOR IMPROVING CMOS DEVICE RELIABILITY USING COMBINATIONS OF INSULATING MATERIALS

Application Number :

Date :

First Named Applicant: Haining Yang

Confirmation Number:

Attorney Docket Number: FIS920040194US1

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Submitted By:	Elec. Sign.	Sign. Capacity
James J. Cioffi Registered Number: 51,564	/James J. Cioffi/	Attorney

Documents being submitted:	Files
us-fee-sheet	FIS920040194US1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-ids	FIS920040194US1-usidst.xml
	us-ids.dtd
	us-ids.xsl
us-request	FIS920040194US1-usrequ.xml
	us-request.dtd
	us-request.xsl
us-assignment	FIS920040194US1-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	fis920040194asg1.tif
	fis920040194asg2.tif
	fis920040194asgC1.tif
	fis920040194asgC2.tif
us-declaration	fis920040194dec1.tif
us-declaration	fis920040194dec2.tif
us-declaration	fis920040194dec3.tif
application-body	fis920040194ABX.xml
	application-body.dtd
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	Image2.tif
	Image3.tif
	Image4.tif
	Image5.tif
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isogrk1.ent
	isogrk2.ent
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	isogrk4.ent
	isolat1.ent
	isolat2.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
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	isotech.ent
	mathml2.dtd

	mathml2-qname-1.mod
	mmlalias.ent
	mmlextra.ent
	soextblx.dtd
	us-application-body.xsl
	wipo.ent
application-body-pdf-wrap	fis920040194ABX-pdf-wrap.xml
abstract-pdf	fis920040194ABX-abst.pdf
claims-pdf	fis920040194ABX-clms.pdf
description-pdf	fis920040194ABX-desc.pdf
drawings-pdf	fis920040194ABX-draw.pdf

Comments

ASSIGNEE NAME: International Business Machines Corporation ASSIGNEE ADDRESS: New Orchard Road, Armonk, NY 10504